



25th IEEE International Symposium on Asynchronous Circuits and Systems

Paper Deadlines

- Regular papers: 7 December 2018 (abstract registration)
14 December 2018 (paper submission)
30 January 2019 (notification of acceptance)
8 March 2019 (final version)
- Short papers: 28 February 2019 (paper submission)
15 March 2019 (notification of acceptance)
29 March 2019 (final version)

See website for details and updates

Call for Papers

The International Symposium on Asynchronous Circuits and Systems (ASYNC) is the premier forum for researchers and Industry to present their latest insights and results in asynchronous VLSI computing. Asynchronous VLSI computations are at the heart of recent deep learning and neuromorphic designs, and well-suited for distributed tasks in fast or low-energy processing and communication.

We invite you to submit a **6-8 page regular paper** with original scientific work relevant to ASYNC, in IEEE conference format (double-column, 10pt or larger). Author information must be omitted from reviewers. Accepted papers must be presented and will be published in the Symposium Proceedings and the IEEE Xplore Digital Library.

We also encourage you to submit **1-2 page papers for demo-poster-ideas** with a demo or poster abstract or with "fresh ideas" to try out live. These go through a light-weight review. Accepted papers must be presented and will be distributed as handouts at the Symposium.

We solicit **1-2 page papers from Industry** on state-of-the-art integration of asynchronous designs to existing or emerging technologies. These must follow the format of a regular paper, but will go through a separate light-weight review process. Accepted papers must be presented and will be published in the Symposium Proceedings and the IEEE Xplore Digital Library.

Website

<http://async2019.jp/>

General Chairs

Tomohiro Yoneda

National Institute of Informatics, Japan
yoneda@nii.ac.jp

Masashi Imai

Hirosaki University, Japan
miyabi@eit.hirosaki-u.ac.jp

Program Chairs

Marly Roncken

Portland State University, USA
mroncken@pdx.edu

Andrey Mokhov

Newcastle University, UK
andrey.mokhov@newcastle.ac.uk

The C-element logo for ASYNC 2019 contains the crest of the Tsugaru clan – the samurai who ruled the domain around Hirosaki Castle under the Edo period Tokugawa shogunate. For more information, see https://en.wikipedia.org/wiki/Tsugaru_clan.